

Notice of References Cited	Application/Control No. 10/815,961	Applicant(s)/Patent Under Reexamination TRAN ET AL.	
	Examiner Pierre-Michel Bataille	Art Unit 2186	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	IBM Technical Discl. Bulletin vol.28 No. 12 May 1986, pp. 5329-5333, "High-Speed Processor Bus Arbitration".
*	V	C. K. Tang, "Cache System Design in the Tightly Coupled Multiprocessor System," Proceedings of the National Computer Conference, New York, Jun. 7-10, 1976, vol. 45, pp. 749-753.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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